

Electronics

Surface Mount Two-Way Power Divider, 5 - 1000 MHz

Features

- **Fully Hermetic Package** •
- Low Loss: 0.3 dB Typical •
- Amplitude Balance: 0.05 dB Typical •
- Impedance: 50 Ohms Nominal •
- Maximum Power Rating or Input Power: 1 Watt Max. •
- Internal Load Dissipation: 0.05 Watts Max.
- MIL-STD-202 Screening Available •

Description

A Power Divider is ideally a lossless reciprocal device which can also perform vector summation of two or more signals and thus is sometimes called a power combiner or summer.

Pin Configuration

Pin No.	Function	Pin No.	Function	
1	Σ	3	GND	
2	Output C	4	Output D	

MARK 080 (20) 205 5 21)

SF-1

GROUND TAS

305

75)

430 (10.92) 510:020 (12.95+0.51)

ORIENTATION

Dimensions in 0 are minim Unless Otherwise Noted: XXX + (0.010

XX = +0.02 WEGHT (APPROX: 0.07 OUNCES 2 GRAMS

Electrical Specifications¹: $T_A = -55^{\circ}C$ to +85°C

Parameter	Test Conditions	Frequency	Units	Min	Тур	Max
Insertion Loss	Less Coupling	5 - 500 MHz 500 - 1000 MHz	dB dB			0.5 1.0
Isolation	-	5 - 500 MHz 500 - 1000 MHz	dB dB	20 16		
Amplitude Balance	_	5 - 1000 MHz	dB	-	_	0.3
Phase Balance	_	5 - 500 MHz 500 - 1000 MHz	0 0			2 3
VSWR	All Ports	10 - 500 MHz 5 - 1000 MHz	Ratio Ratio	_	_	1.4:1 1.6:1

1. All specifications apply with 50 ohm source and load impedance.

This product contains elements protected by United States Patent Number 3,428,920.

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ua 32

170 MAX



016 W±002(0.41 +0.05) X 010 T +001 10.25 +.030

060

(1.52)

385 • 020 (9 8 • 0.51)

DSS-327 V4

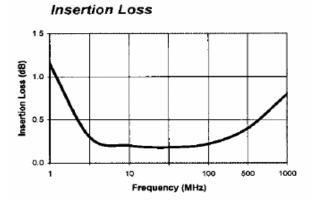


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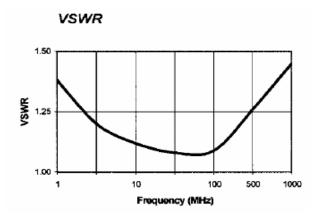


Typical Performance Curves

Insertion Loss

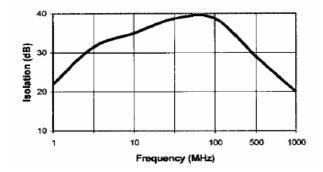






Isolation

Isolation



Ordering Information

Part Number	Package		
DSS-327 PIN	SF-1		

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